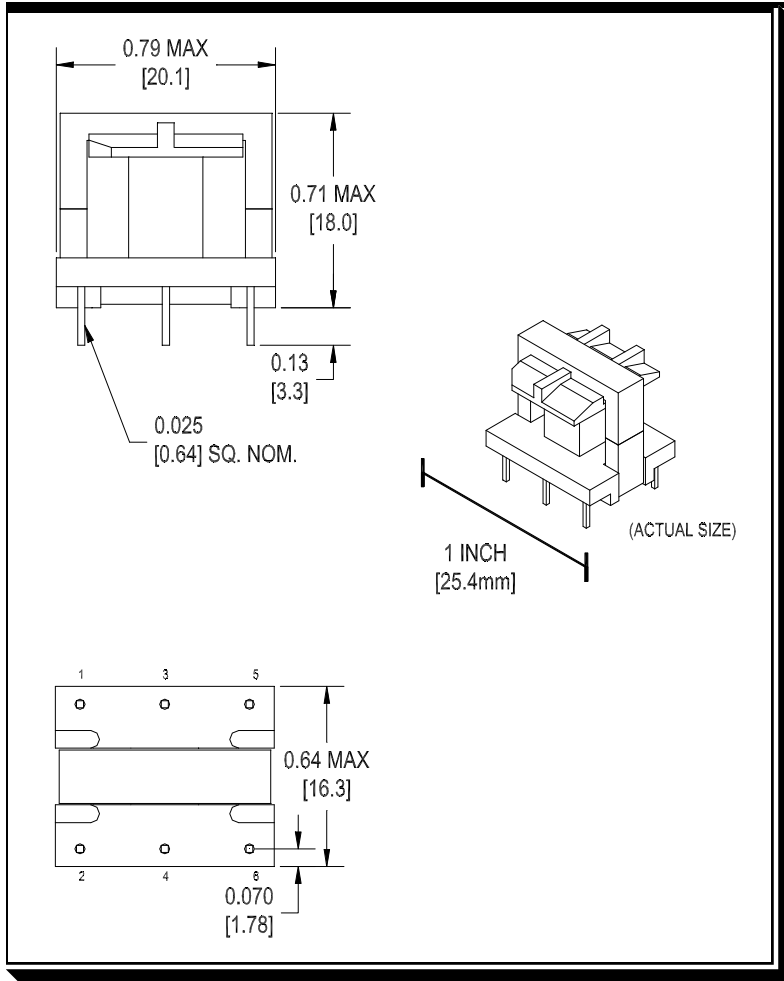


CSD 2



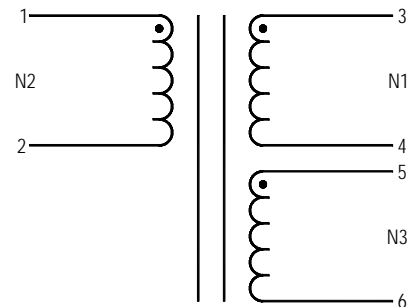
PLATFORM  **FEATURES**

- * Efficient, Economical
- * Frequencies up to 500kHz
- * Industry Standard Footprint
- * 2,600 VRMS Isolation
- * VDE, IEC, UL, CSA Compatible
- * UL Class 130(B) Insulation⁽¹⁾
- * Custom Versions Available

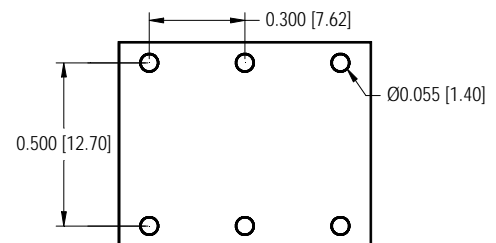
Samples Available on Request

techsales@cramercoil.com
(262) 268-2150 (Inside Sales)
(262) 268-4100 (FAX)

WINDING SCHEMATIC



SUGGESTED PCB LAYOUT⁽²⁾



Note:
Unless otherwise specified, tolerances are
x.xxx = 0.003 [0.08]

Notes:
Dimensions: inches [millimeters] (entire page)

CSD 2 - Series				
Part Number:	2-050	2-100	2-150	2-200
Turns Ratio	2:1:1	1:1:1	1:1.5:1.5	1:2:2
L (mH)	1.00	1.00	1.00	1.00
LL (µH)	6.0	6.0	6.0	6.0
CC (pF)	30	30	30	30
CC (pF)	30	30	30	30
Rp (ohms)	0.50	0.50	0.50	0.50
Rs (ohms)	0.21	0.43	1.25	1.70
Rs (ohms)	0.30	0.80	1.75	2.50
Ipri (A max.)	1.25	1.25	1.25	1.25
Isec	1.25	1.25	0.75	0.75
Isec	1.25	1.25	0.75	0.75
ET Const. (Vµs)	350	350	350	350
KP ⁽³⁾⁽⁴⁾	1090	1090	1090	1090
Hipot	2,600	2,600	2,600	2,600

(1) System designation C5; File #E110339.
 (2) Final responsibility for the correct PCB layout resides with the user.
 (3) To avoid saturating the transformer the peak AC flux (Bpk) must be below 0.32T.
 (4) Calculate Bpk using $Bpk = Et / Kp \cdot Kd$. Where $Et = Vpk \cdot (D/F) \cdot 10^3$. Et = Volt Microseconds, Vpk = Peak Voltage, D = Duty Cycle (decimal), F = Frequency (kHz), $Kd = 1$ for Unipolar and 2 for Bipolar, Kp = from table.