

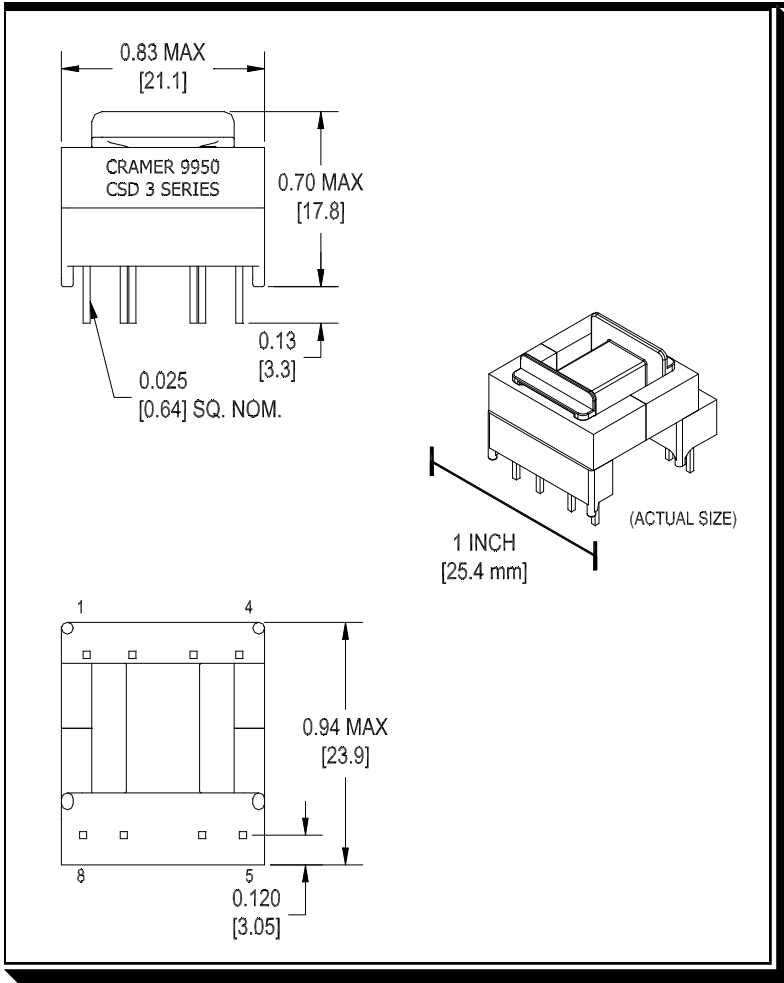
# CSD 3

**PLATFORM**  **FEATURES**

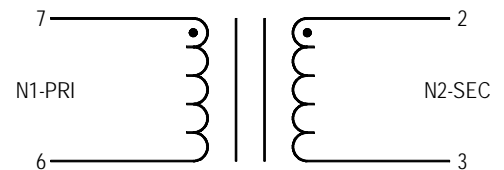
- \* Efficient, Economical
- \* Frequencies up to 500kHz
- \* Industry Standard Footprint
- \* 3,750 VRMS Isolation
- \* VDE, IEC, UL, CSA Compatible
- \* UL Class 130(B) Insulation<sup>(1)</sup>
- \* Custom Versions Available

**Samples Available on Request**

techsales@cramercoil.com  
(262) 268-2150 (Inside Sales)  
(262) 268-4100 (FAX)



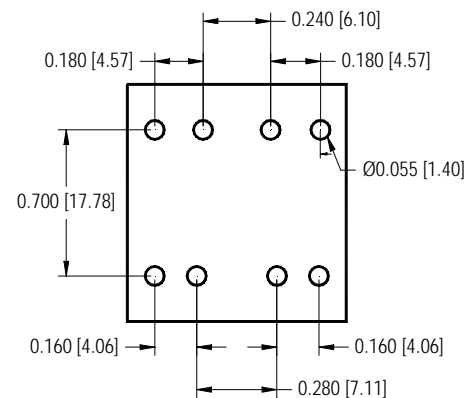
**WINDING SCHEMATIC**



Notes:  
Dimensions: inches [millimeters] (entire page)

<b>CSD 3 - Series</b>				
<b>Part Number:</b>	<b>3-050</b>	<b>3-100</b>	<b>3-150</b>	<b>3-200</b>
Turns Ratio	2:1	1:1	1:1.5	1:2
L (mH)	3.20	3.20	3.20	3.20
LL (μH)	10.0	10.0	10.0	10.0
CC (pF)	35	35	35	35
CC (pF)	—	—	—	—
Rp (ohms)	0.48	0.48	0.48	0.48
Rs (ohms)	0.36	0.75	1.14	1.58
Rs (ohms)	—	—	—	—
Ipri (A max.)	1.25	1.25	1.25	1.25
Isec	1.00	1.00	1.00	1.00
Isec	—	—	—	—
ET Const. (Vμs)	500	500	500	500
KP <sup>(3)(4)</sup>	1570	1570	1570	1570
Hipot	3,750	3,750	3,750	3,750

**SUGGESTED PCB LAYOUT<sup>(2)</sup>**



Note:  
Unless otherwise specified, tolerances are  
x.xxx = 0.003 [0.08]

(1) System designation C5; File #E110339.  
(2) Final responsibility for the correct PCB layout resides with the user.  
(3) To avoid saturating the transformer the peak AC flux (Bpk) must be below 0.32T.  
(4) Calculate Bpk using  $Bpk = Et / Kp \cdot Kd$ . Where  $Et = Vpk \cdot (D/F) \cdot 10^3$ .  $Et$  = Volt Microseconds,  $Vpk$  = Peak Voltage,  $D$  = Duty Cycle (decimal),  $F$  = Frequency (kHz),  $Kd$  = 1 for Unipolar and 2 for Bipolar,  $Kp$  = from table.