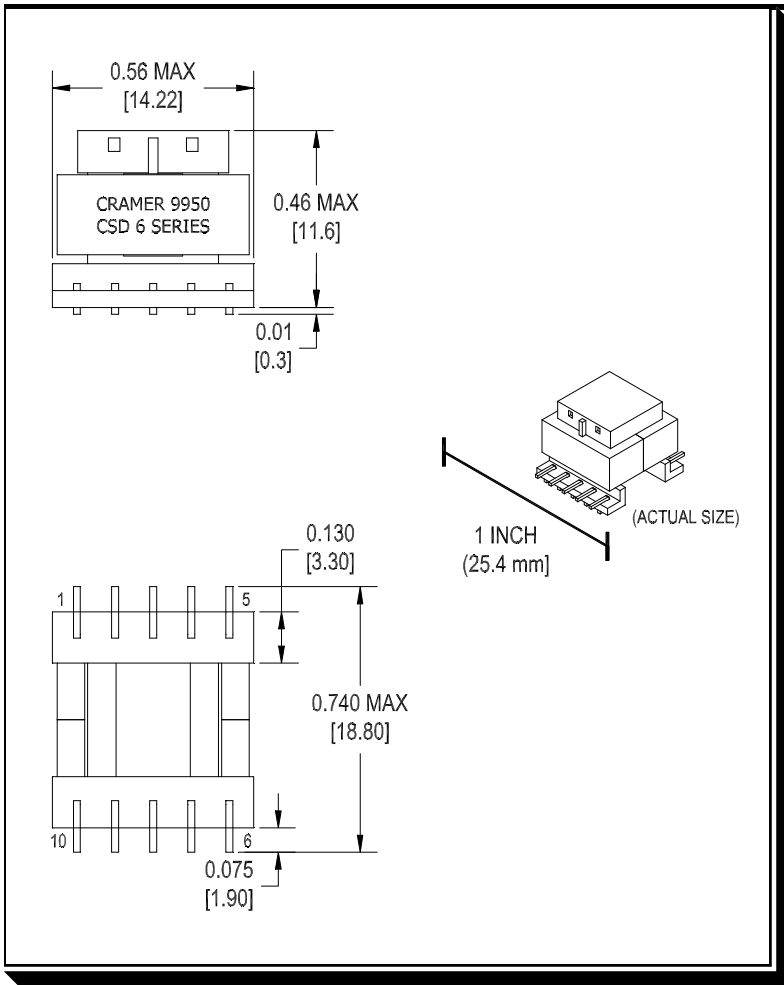


# CSD 6



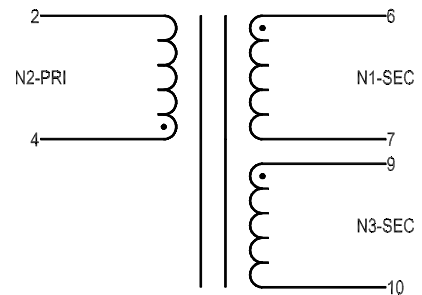
**PLATFORM**  **FEATURES**

- \* Efficient, Economical
- \* Frequencies up to 500kHz
- \* Industry Standard Footprint
- \* 2,600 VRMS Isolation
- \* VDE, IEC, UL, CSA Compatible
- \* UL Class 130(B) Insulation<sup>(1)</sup>
- \* Custom Versions Available

**Samples Available on Request**

techsales@cramercoil.com  
(262) 268-2150 (Inside Sales)  
(262) 268-4100 (FAX)

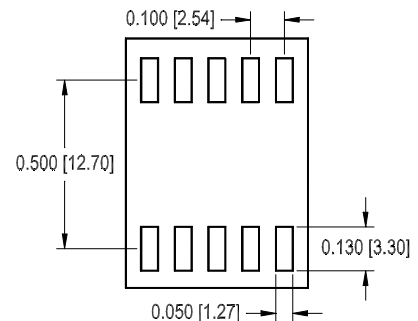
**WINDING SCHEMATIC**



Notes:  
Dimensions: inches [millimeters] (entire page)

<b>CSD 6 - Series</b>				
<b>Part Number:</b>	<b>6-050</b>	<b>6-100</b>	<b>6-150</b>	<b>6-200</b>
Turns Ratio	2:1:1	1:1:1	1:1.5:1.5	1:2:2
L (mH)	0.75	0.75	0.75	0.75
LL (μH)	5.50	5.50	5.50	5.50
CC (pF)	40	40	40	40
CC (pF)	40	40	40	40
Rp (ohms)	0.53	0.53	0.53	0.53
Rs (ohms)	0.16	0.47	1.17	2.30
Rs (ohms)	0.20	0.62	1.50	2.90
Ipri (A max.)	0.75	0.75	0.75	0.75
Isec	1.00	0.75	0.50	0.37
Isec	1.00	0.75	0.50	0.37
ET Const. (Vμs)	158	158	158	158
KP <sup>(3)(4)</sup>	490	490	490	490
Hipot	2,600	2,600	2,600	2,600

**SUGGESTED PCB LAYOUT<sup>(2)</sup>**



Note:  
Unless otherwise specified, tolerances are  
x.xxx = 0.003 [0.08]

(1) System designation C5; File #E110339.  
(2) Final responsibility for the correct PCB layout resides with the user.  
(3) To avoid saturating the transformer the peak AC flux (Bpk) must be below 0.32T.  
(4) Calculate Bpk using  $Bpk = Et / Kp \cdot Kd$ . Where  $Et = Vpk \cdot (D/F) \cdot 10^3$ . Et = Volt Microseconds, Vpk = Peak Voltage, D = Duty Cycle (decimal), F = Frequency (kHz), Kd = 1 for Unipolar and 2 for Bipolar, Kp = from table.