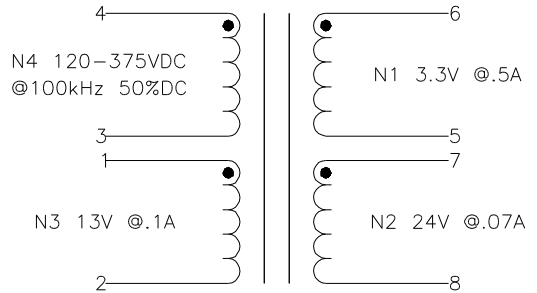


FOR USE WITH
STMicroelectronics
VIPer 20
PRIMARY REGULATION



NOTE:
POLARITY SHOWN IS FOR THE
TRANSFORMER. FOR CORRECT
FLYBACK OPERATION CONNECT
B+ TO PIN 3.

ELECTRICAL SPECIFICATIONS

L @ 1kHz .1V - (3-4) 2.8mH ±10

LL @ 100kHz .1V - (3-4) 76µH TYP, TOL TBD
(WITH 1,2,5-8 SHORTED)

URNS RATIO - ±3

- 3-4:1-2 - 1:0.114
- 3-4:5-6 - 1:0.030
- 3-4:8-7 - 1:0.216

DCR - OHMS TYP, TOL TBD

- 3-4 - 5.23
- 1-2 - 0.559
- 5-6 - 0.017
- 7-8 - 0.558

HIPOT - 5500VAC 1SEC
N1,N2 TO N3,N4

NOTE:
MEETS CONSTRUCTION FOR:
UL 1310, 1950, 1411
IEC 61558-1, 2-17, EN60950

REVISION		DATE	MARKINGS	LOCATION
0	PRELIMINARY	12/15/99	CRAMER EIA DATE CODE CVP 10-010	PIN RAIL SIDE OF CORE
1	CHANGED PIN OUTS	02/22/00	DESCRIPTION 5 VA FLYBACK XFMR 3.3, 24	TOLERANCE UNLESS OTHERWISE SPECIFIED 2 PLACE DECIMALS ±.010 3 PLACE DECIMALS ±.005 FRACTIONS ±1/64 ANGLES ±1/2°
A	RELEASED	01/12/05	CUSTOMER CRAMER STANDARD	
			G:\DATA\SPECS\ENG\CVP\10-010	CUSTOMER PART NUMBER
			DR. MEC	DATE 12/15/99
			APPD. JDB	DATE 12/15/99
			CRAMER PART NUMBER CVP 10-010	
			REV A	