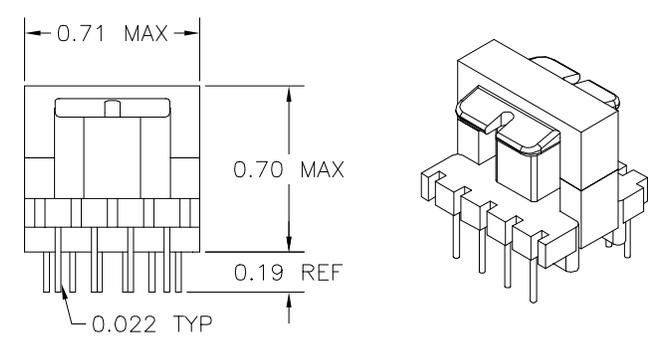


1 2 3 4 5 6

A

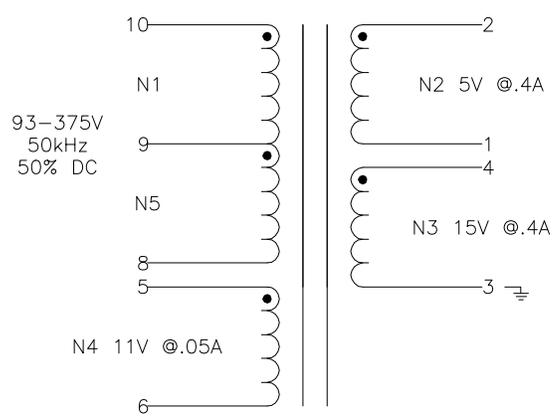
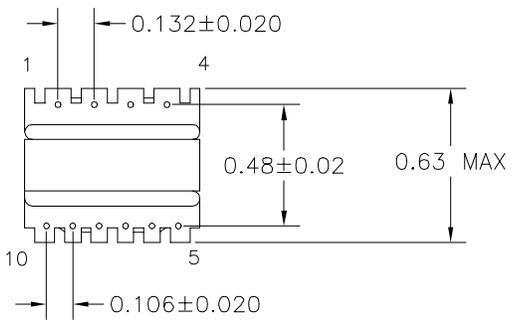


A

B

B

C



C

D

D

E

NOTE:  
PINS 1&4 ARE TIED ON THE PC BOARD.

E

ELECTRICAL SPECIFICATIONS

L @ 1kHz 10mA - (10-8) 2.53mH ±10%

LL @ 100kHz 10mA - (10-8) 17µH TYP, TOL TBD  
(WITH 1-6 SHORTED)

URNS RATIO - ±3%

N1/N5:N2 - 1:0.062

N1/N5:N3 - 1:0.108

N1/N5:N4 - 1:0.138

DCR - OHMS TYP, TOL TBD

N1/N5 - 2.22

N2 - 0.054

N3 - 0.101

N4 - 0.342

HIPOT - 4000VAC 1SEC

N1,N4,N5 TO N2,N3

F

F

G

G

H

H

REVISION		DATE	MARKINGS	LOCATION	
A	RELEASED	03/27/01	544-EIA DATE CODE VP11-008	TOP OF CORE	
B	UPDATED ELECTRICALS	04/03/01	DESCRIPTION	TOLERANCE UNLESS OTHERWISE SPECIFIED 2 PLACE DECIMALS ±.010 3 PLACE DECIMALS ±.005 FRACTIONS ±1/64 ANGLES ±1/2°	PROPRIETARY INFORMATION PROPERTY OF CRAMER COIL & TRANSFORMER CO. DUPLICATION AND/OR DISTRIBUTION BY PERMISSION ONLY
C	ADDED VOLTAGES TO SCHEMATIC & NOTE	07/30/01	CUSTOMER		
D	UPDATED PIN TO PIN & HEIGHT DIMENSIONS	06/26/03			
			G:\DATA\SPECS\ENG\CVP\11-008	CUSTOMER PART NUMBER	REV
			DR. CAM	DATE 03/27/01	
			APPD. JDB	DATE 03/28/01	
				CRAMER PART NUMBER CVP 11-008	REV D

J

J

1 2 3 4 5 6