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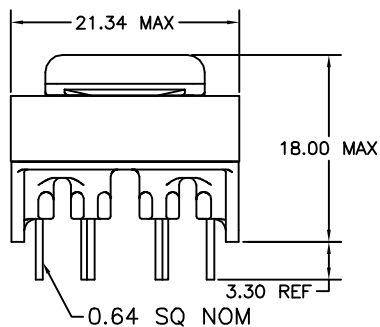
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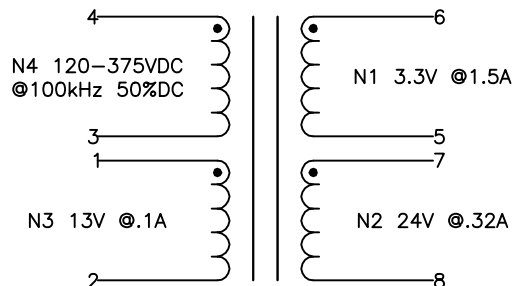
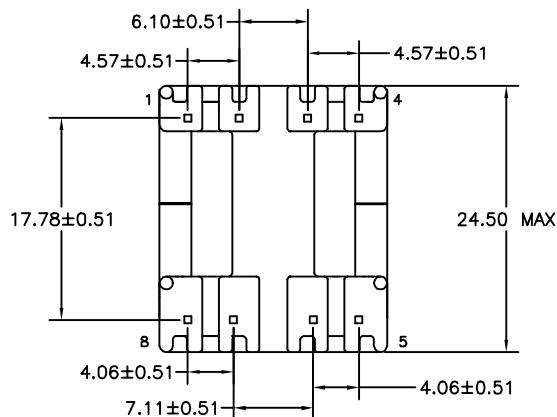
A



FOR USE WITH
STMicroelectronics
VIPer 50
PRIMARY REGULATION

B

C



D

E

NOTE:
POLARITY SHOWN IS FOR THE
TRANSFORMER. FOR CORRECT
FLYBACK OPERATION, CONNECT
B+ TO PIN 3.

ELECTRICAL SPECIFICATIONS

L @ 1kHz .1V - (3-4) 900µH ±10%

LL @ 100kHz .1V - (3-4) 21µH TYP, TOL TBD
(WITH 1,2,5-8 SHORTED)

URNS RATIO - ±3%
3-4:1-2 - 1:0.148
3-4:5-6 - 1:0.045
3-4:7-8 - 1:0.295

DCR - OHMS TYP, TOL TBD
3-4 - 1.51
1-2 - 0.213
5-6 - 0.007
7-8 - 0.321

HIPOT - 5500VAC 1SEC
N1,N2 TO N3,N4

NOTE:
MEETS CONSTRUCTION FOR:
UL 1310, 1950, 1411
IEC 61558-1, 2-17, EN60950

H

REVISION		DATE	MARKINGS	LOCATION
0	PRELIMINARY	01/10/00	CRAMER EIA DATE CODE CVP 30-009	PIN RAIL SIDE OF CORE
1	CHANGED PIN OUTS	02/22/00	DESCRIPTION 15 VA FLYBACK,3,3,24	TOLERANCE UNLESS OTHERWISE SPECIFIED 2 PLACE DECIMALS ±.010 3 PLACE DECIMALS ±.005 FRACTIONS ±1/64 ANGLES ±1/2°
2	WIDTH WAS 0.94 MAX	11/19/01	CUSTOMER	
A	RELEASED	01/13/05		
			G:\DATA\SPECS\ENG\CVP\30-009	CUSTOMER PART NUMBER
			DR. CAM	DATE 01/10/00
			APPD. JDB	DATE 01/11/00
				CRAMER PART NUMBER CVP 30-009
				REV A

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