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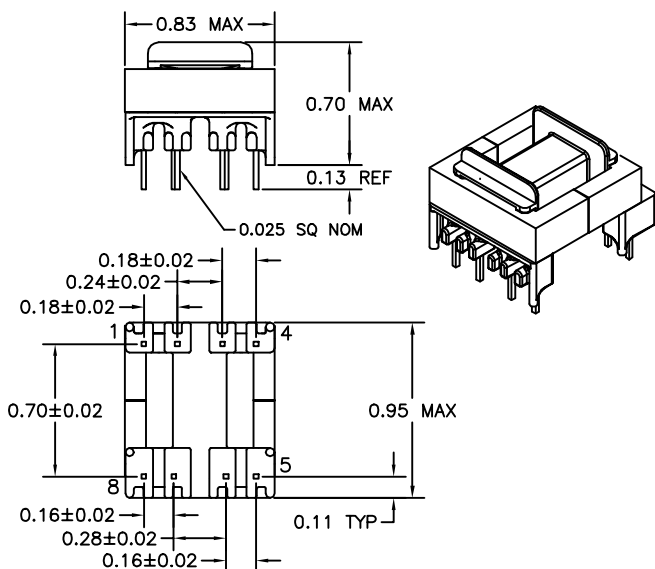
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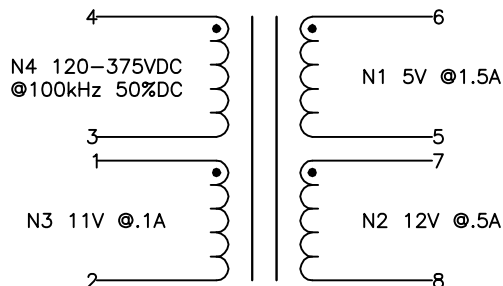
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FOR USE WITH
STMicroelectronics
VIPer 50
SECONDARY REGULATION



ELECTRICAL SPECIFICATIONS

L @ 1kHz .1V - (3-4) 900µH ±10%

LL @ 100kHz .1V - (3-4) 21µH TYP, TOL TBD
(WITH 1,2,5-8 SHORTED)

URNS RATIO - ±3%
3-4:1-2 - 1:0.125
3-4:5-6 - 1:0.057
3-4:7-8 - 1:0.136

DCR - OHMS TYP, TOL TBD
3-4 - 1.52
1-2 - 0.184
5-6 - 0.010
7-8 - 0.048

HIPOT - 4000VAC 1SEC
N1,N2 TO N3,N4

NOTE:
POLARITY SHOWN IS THE TRANSFORMER.
FOR CORRECT FLYBACK OPERATION
CONNECT B+ TO PIN 3.

NOTE:
MEETS CONSTRUCTION FOR:
UL 1310, 1950, 1411
EN60950

| REVISION | | DATE | MARKINGS | LOCATION |
|----------|------------------|----------|------------------------------------|---|
| O | PRELIMINARY | 01/10/00 | CRAMER EIA DATE CODE CVP 30-010 | PIN RAIL SIDE OF CORE |
| 1 | CHANGED PIN OUTS | 02/22/00 | DESCRIPTION 15 VA FLYBACK,5,12 | TOLERANCE UNLESS OTHERWISE SPECIFIED 2 PLACE DECIMALS ±.010 3 PLACE DECIMALS ±.005 FRACTIONS ±1/64 ANGLES ±1/2° |
| A | RELEASED | 02/19/03 | CUSTOMER | |
| | | | | |
| | | | G:\DATA\SPECS\ENG\CVP\30-010 | CUSTOMER PART NUMBER |
| | | | DR. CAM | DATE 01/10/00 |
| | | | APPD. JDB | DATE 01/11/00 |
| | | | | CRAMER PART NUMBER CVP 30-010 |
| | | | | REV A |

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